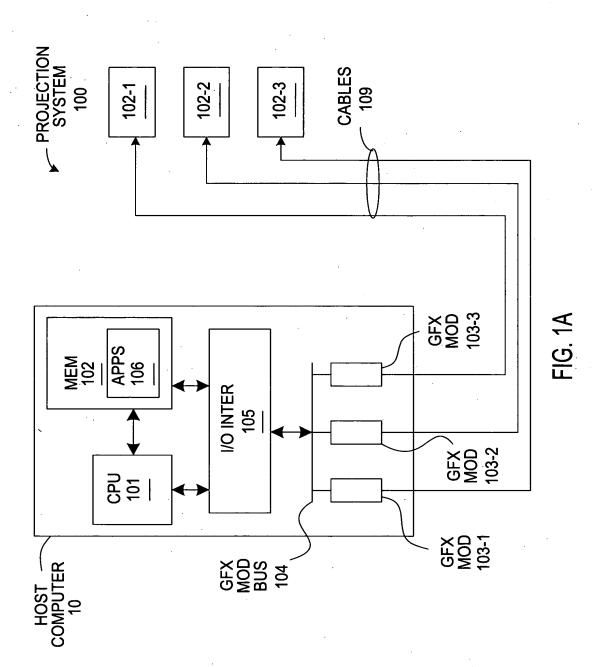
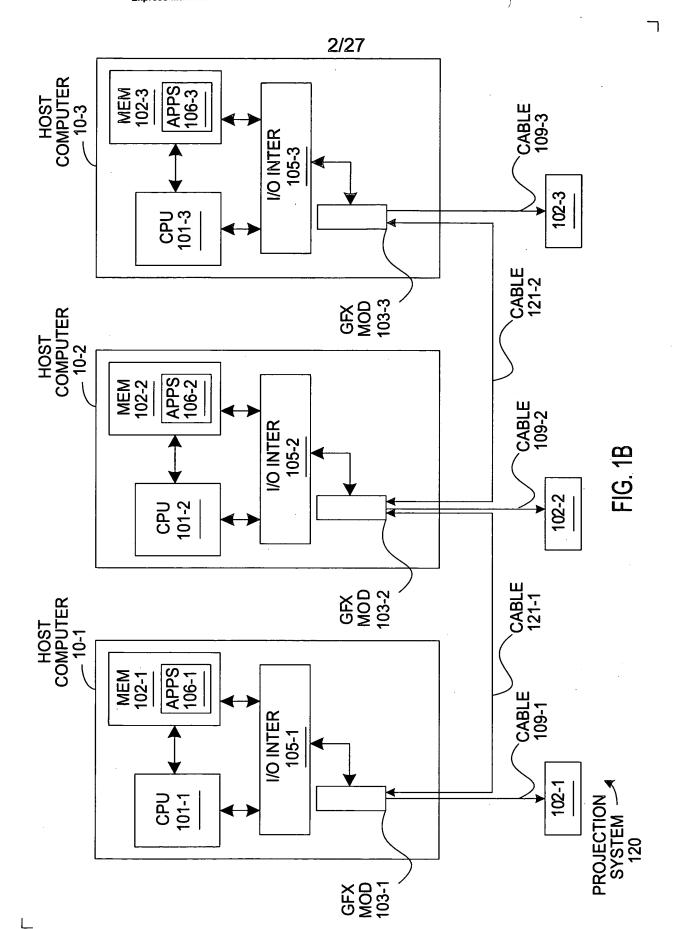
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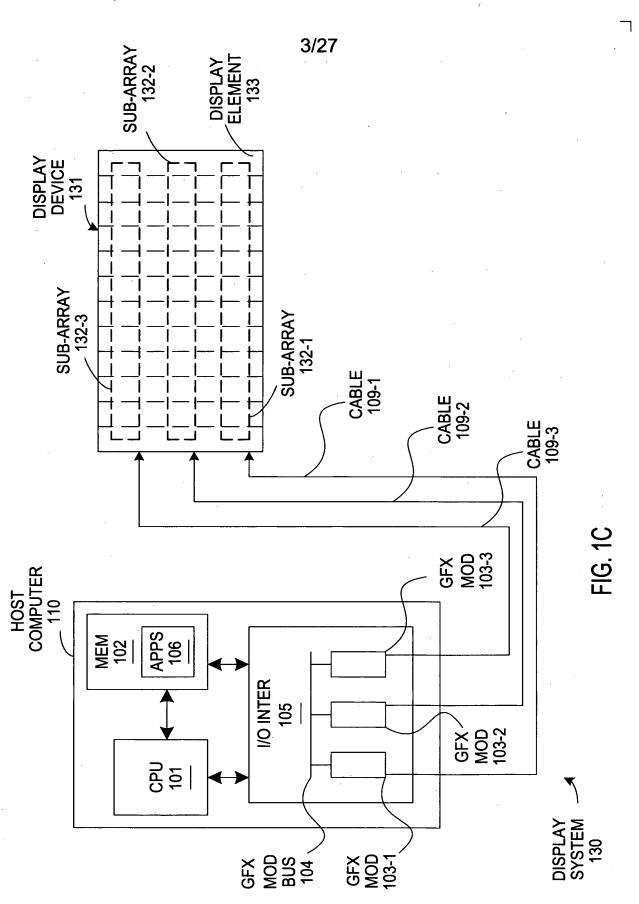


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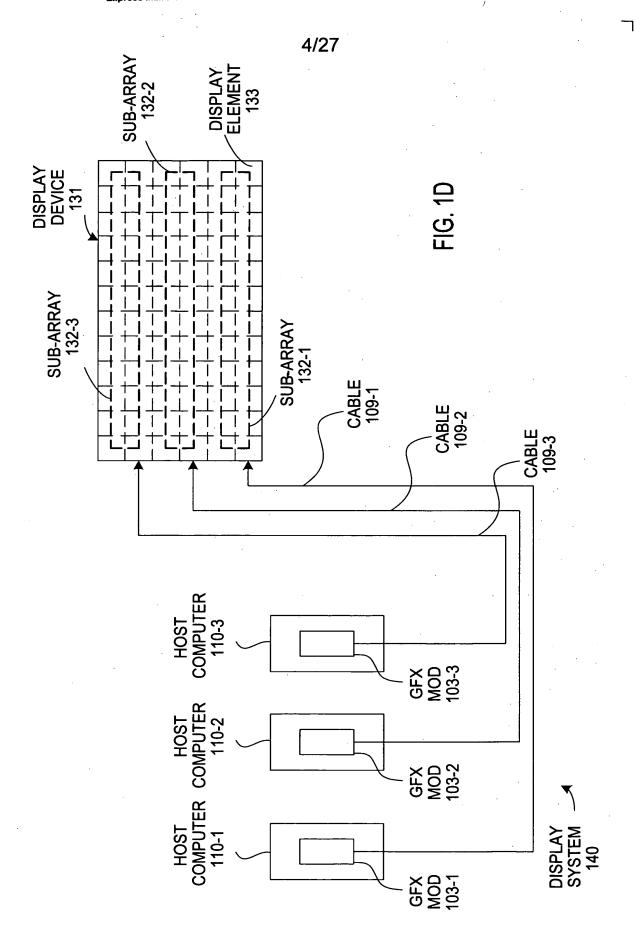
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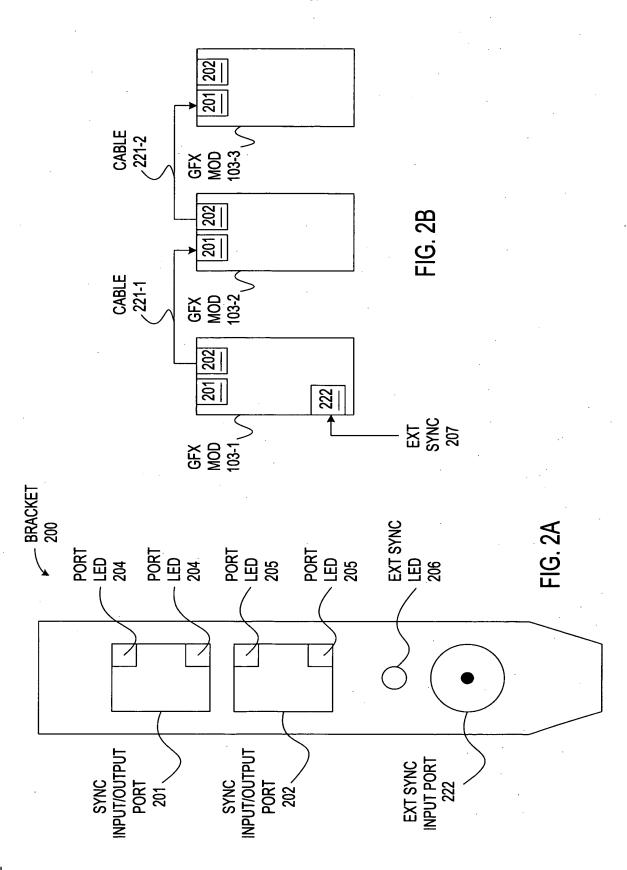
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Inventor: Ian M. Williams, ET AL.
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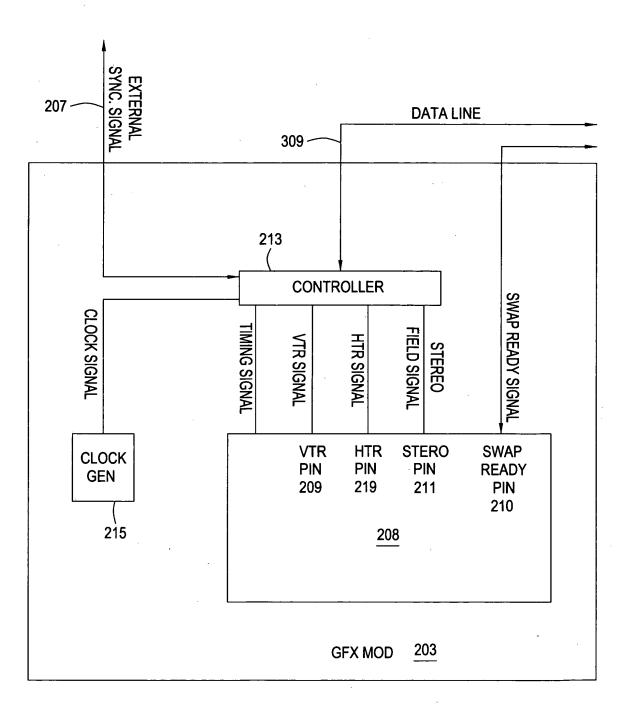
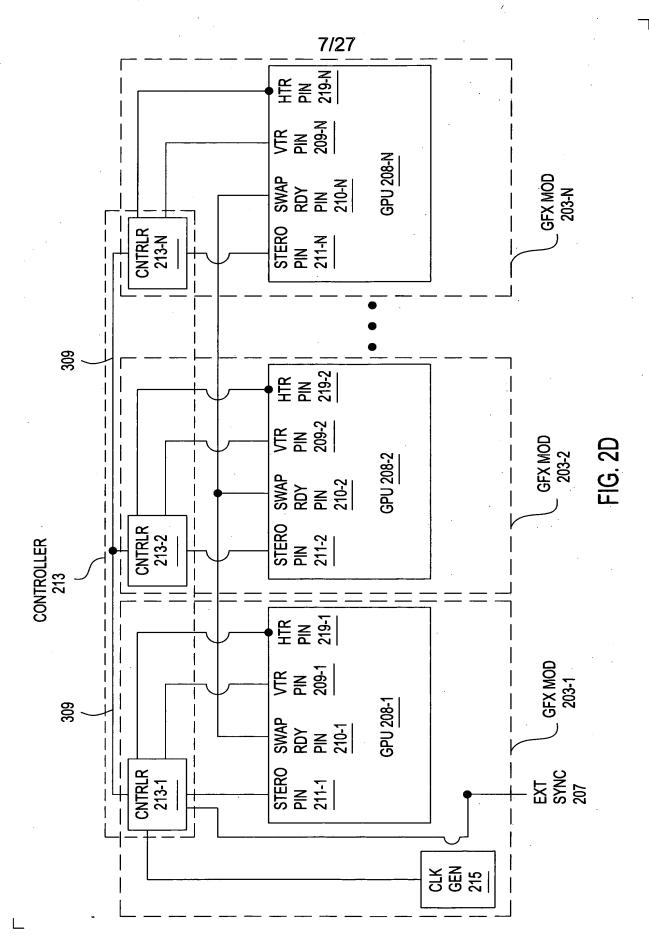


FIG. 2C

Atty Dkt. No.: U.S. Serial No.: Filed: NVDA/P000737 UNKNOWN HEREWITH

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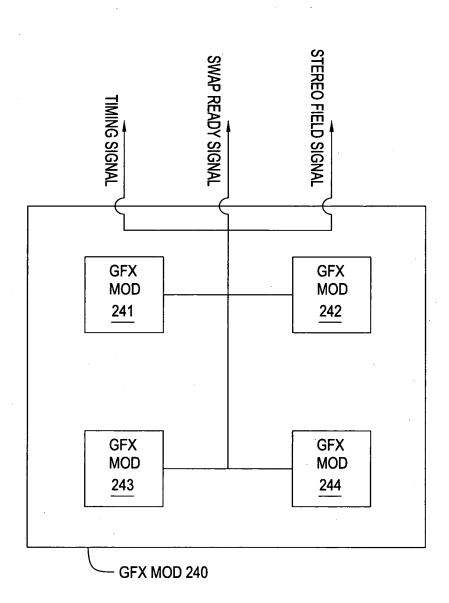


FIG. 2E

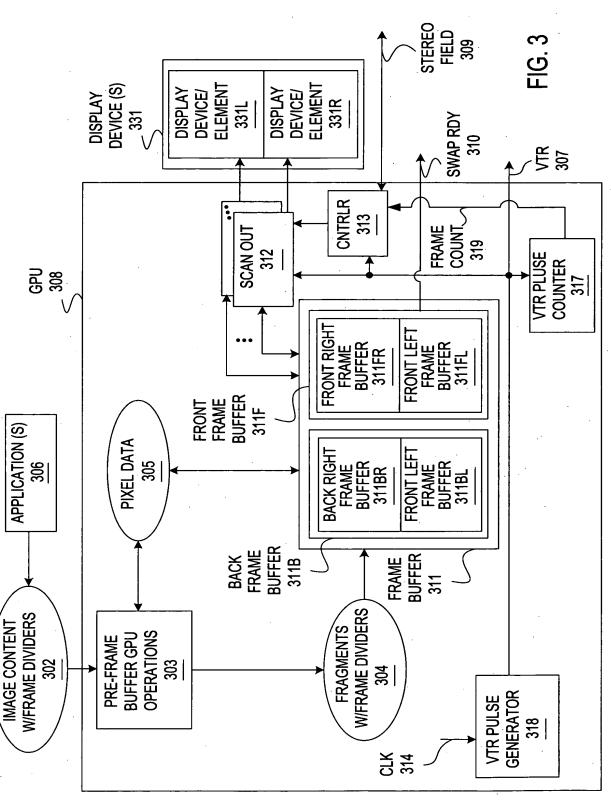
NVDA/P000737 UNKNOWN HEREWITH Atty Dkt. No.: U.S. Serial No.: Filed: Applicant: Title: **NVIDIA Corporation**

Ititle: Method For Synchronizing Graphics Processing Units Inventor: Ian M. Williams, ET AL.

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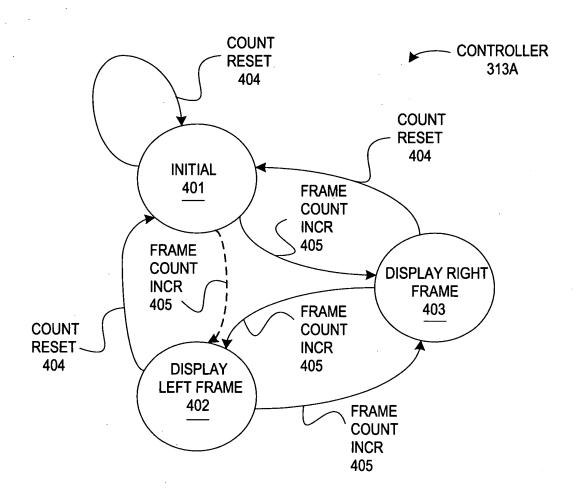
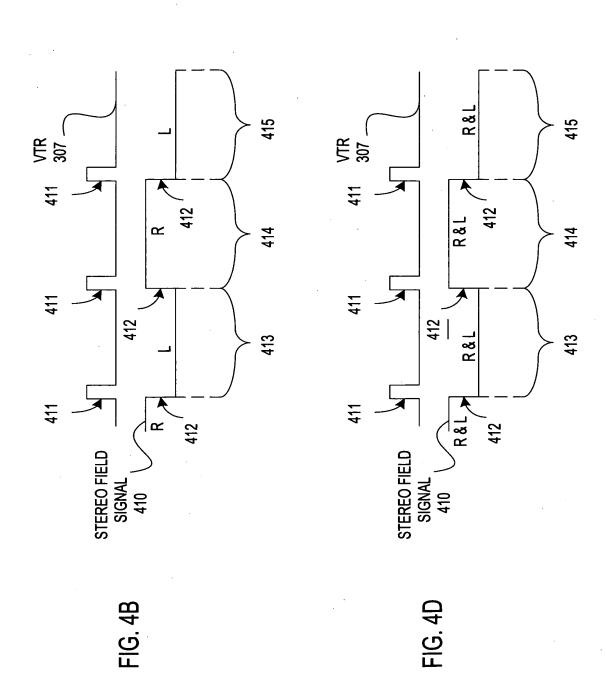


FIG. 4A

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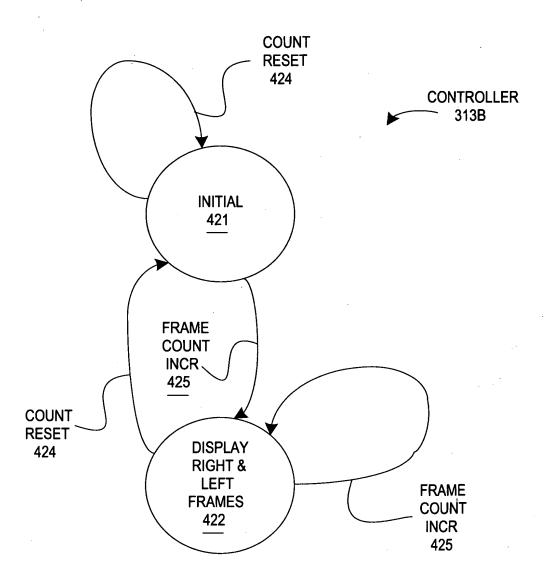


FIG. 4C

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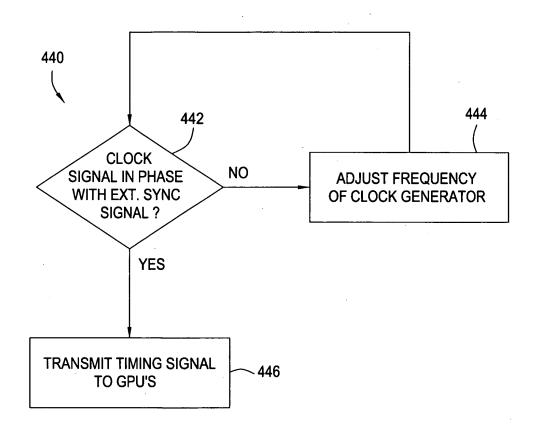


FIG. 4E

Atty Dkt. No.: U.S. Serial No.: NVDA/P000737 UNKNOWN HEREWITH Filed: Applicant: **NVIDIA Corporation**

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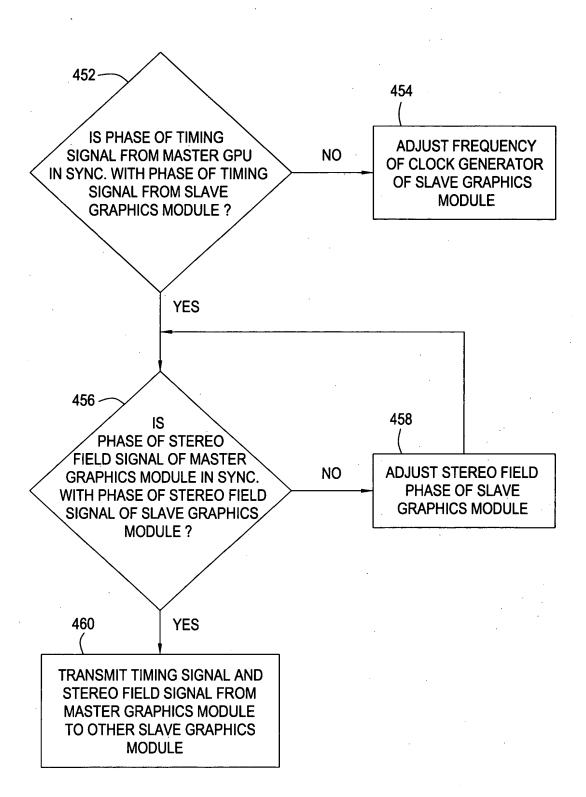


FIG. 4F

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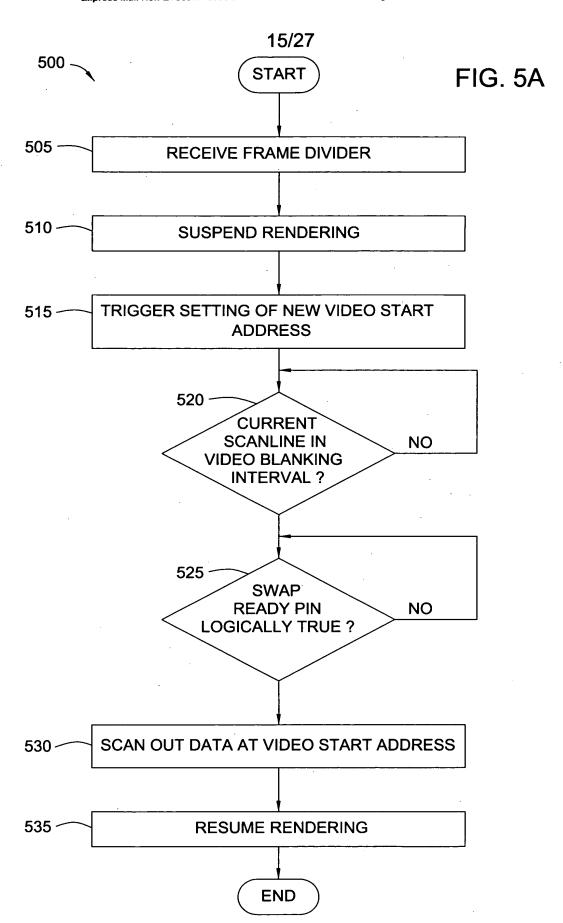
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Atty Dkt. No.: U.S. Serial No.: Filed:

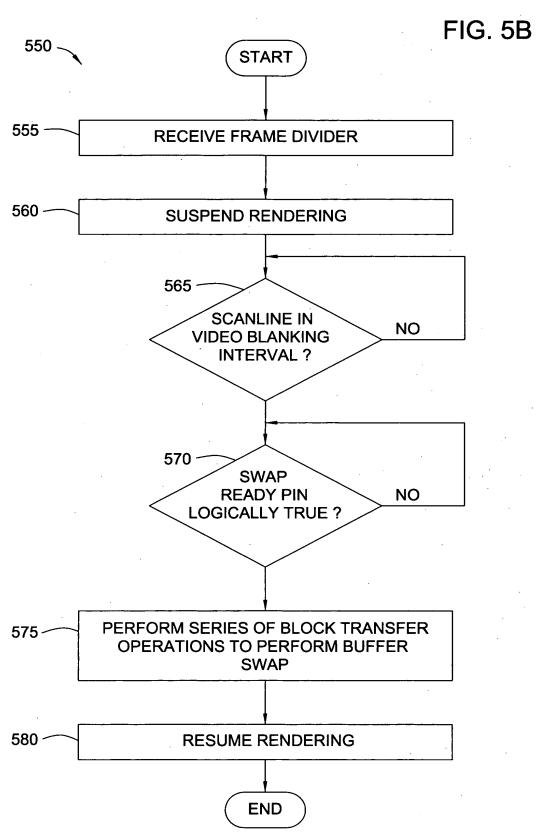
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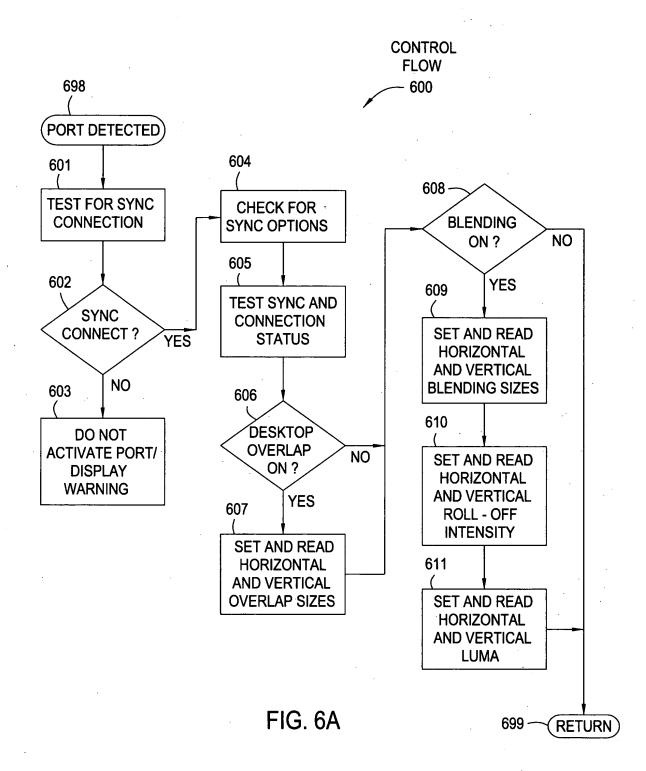
Atty Dkt. No.: U.S. Serial No.: NVDA/P000737 UNKNOWN Filed: HEREWITH Applicant: Title: **NVIDIA Corporation**

Inventor:

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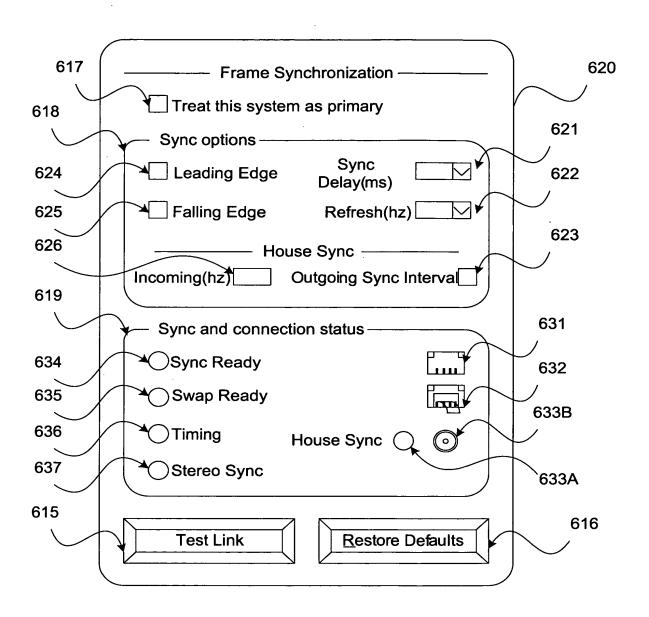
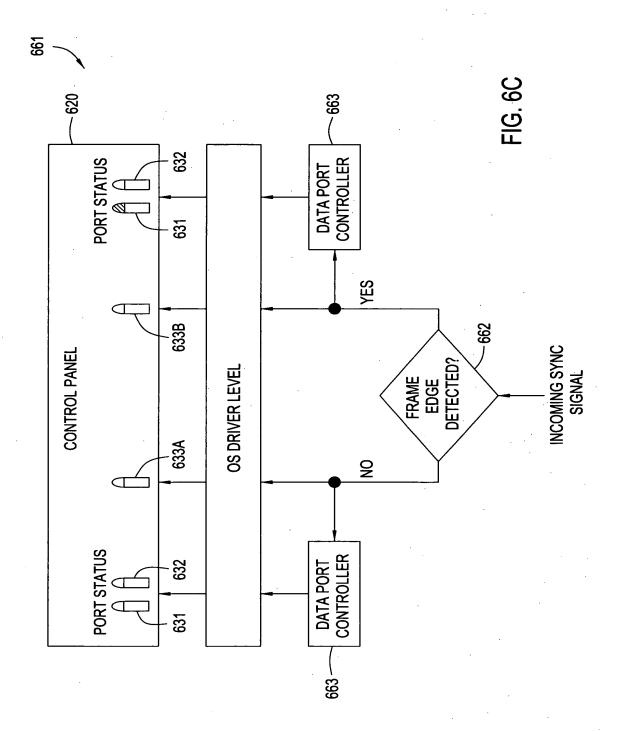
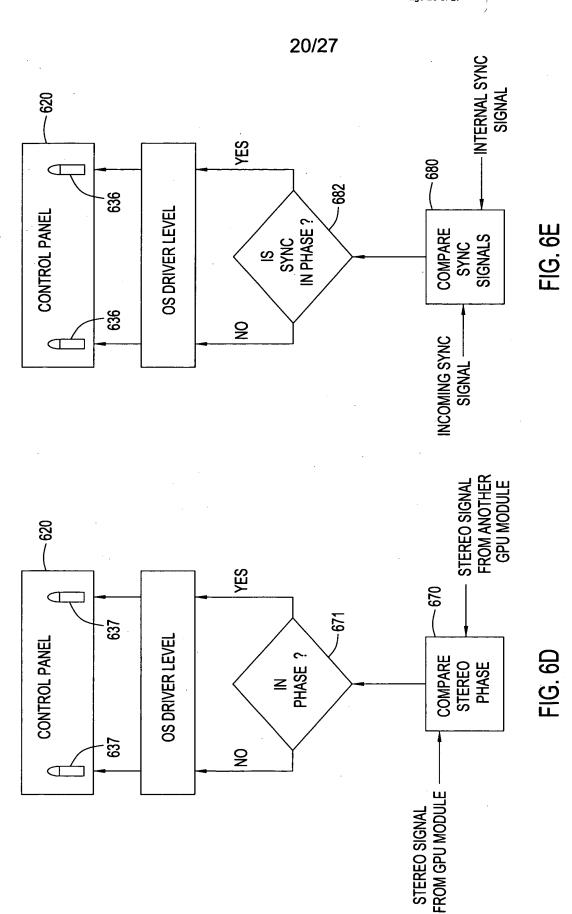


FIG. 6B

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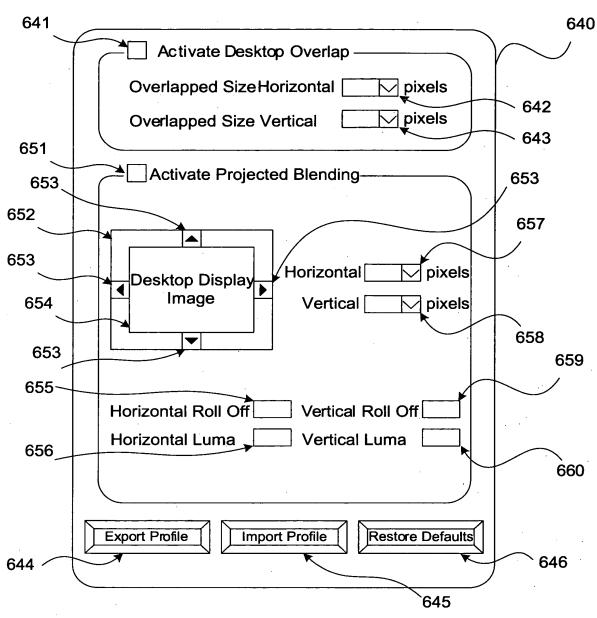
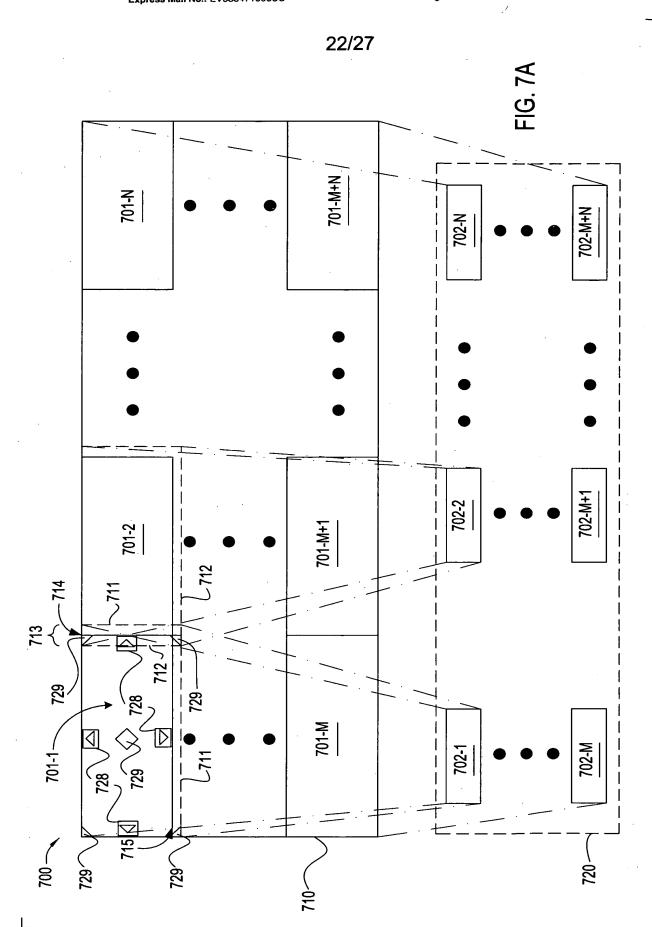


FIG. 6F

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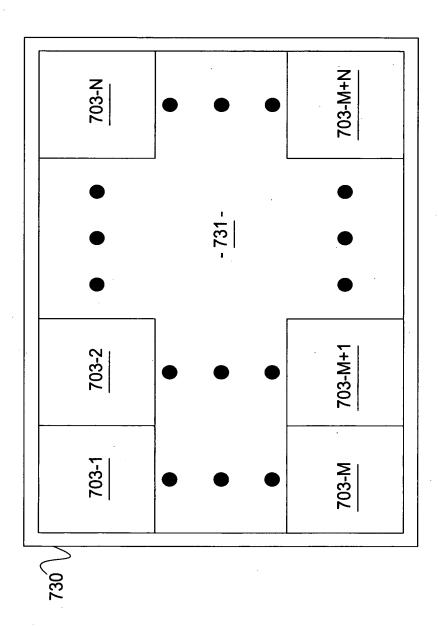
Title: Method For Synchronizing Graphics Processing Units Inventor: Ian M. Williams, ET AL.

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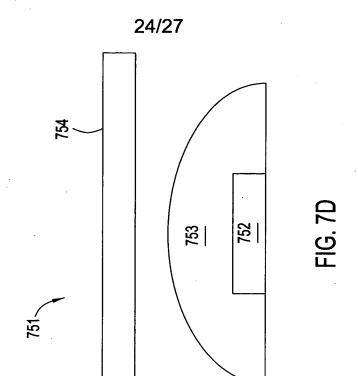


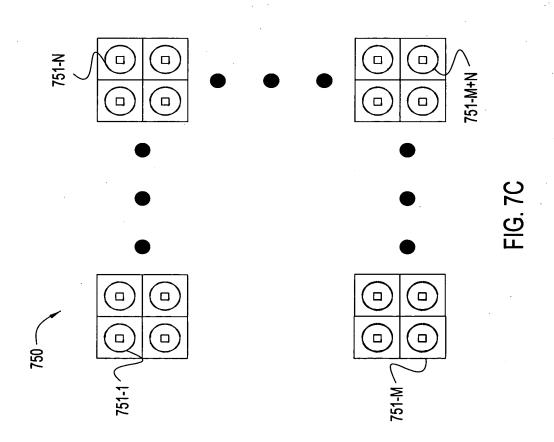
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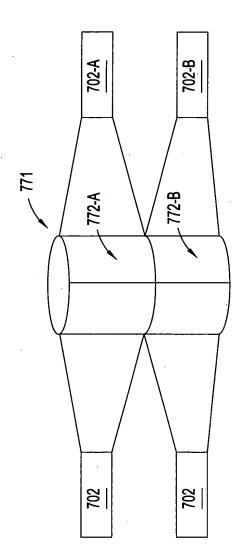


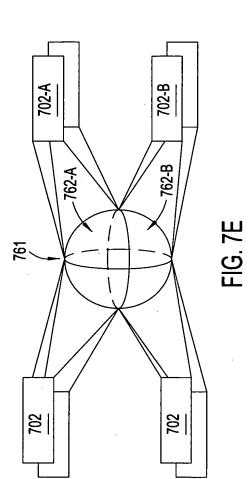
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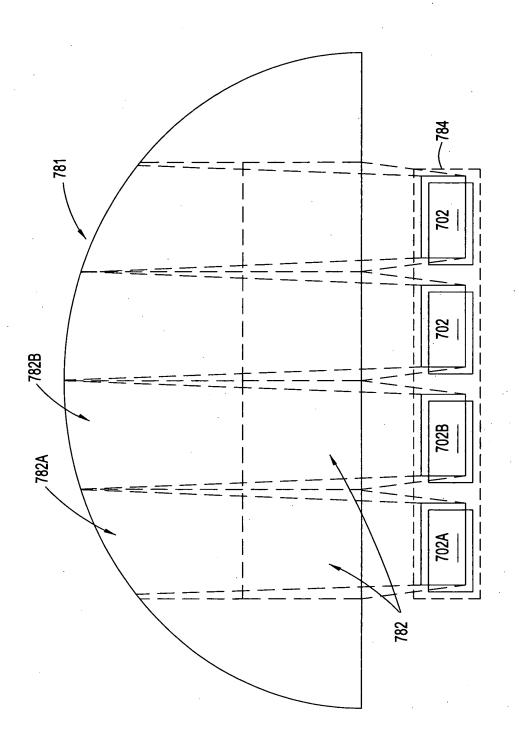
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